

FIG. 2

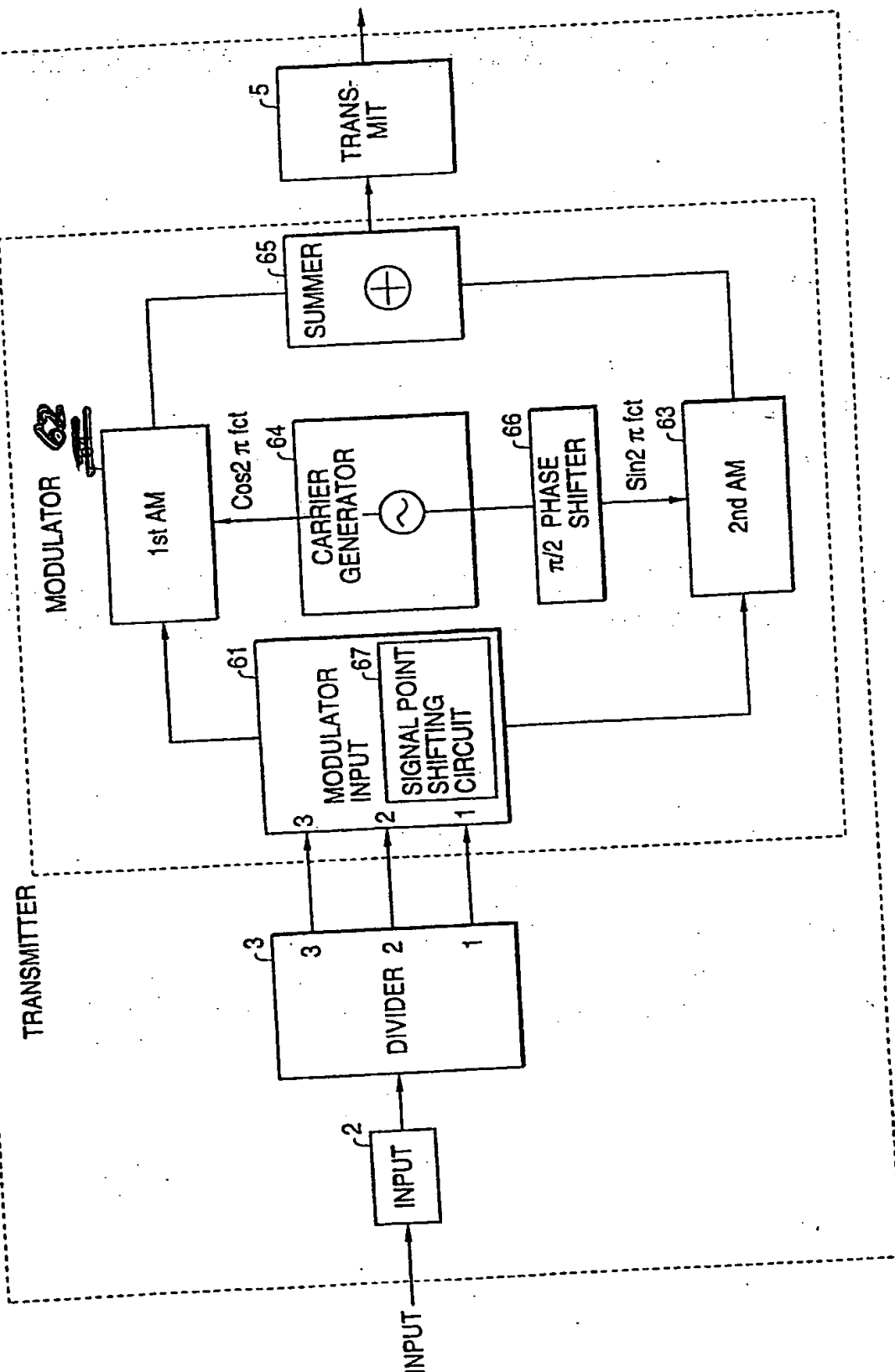


FIG. 10

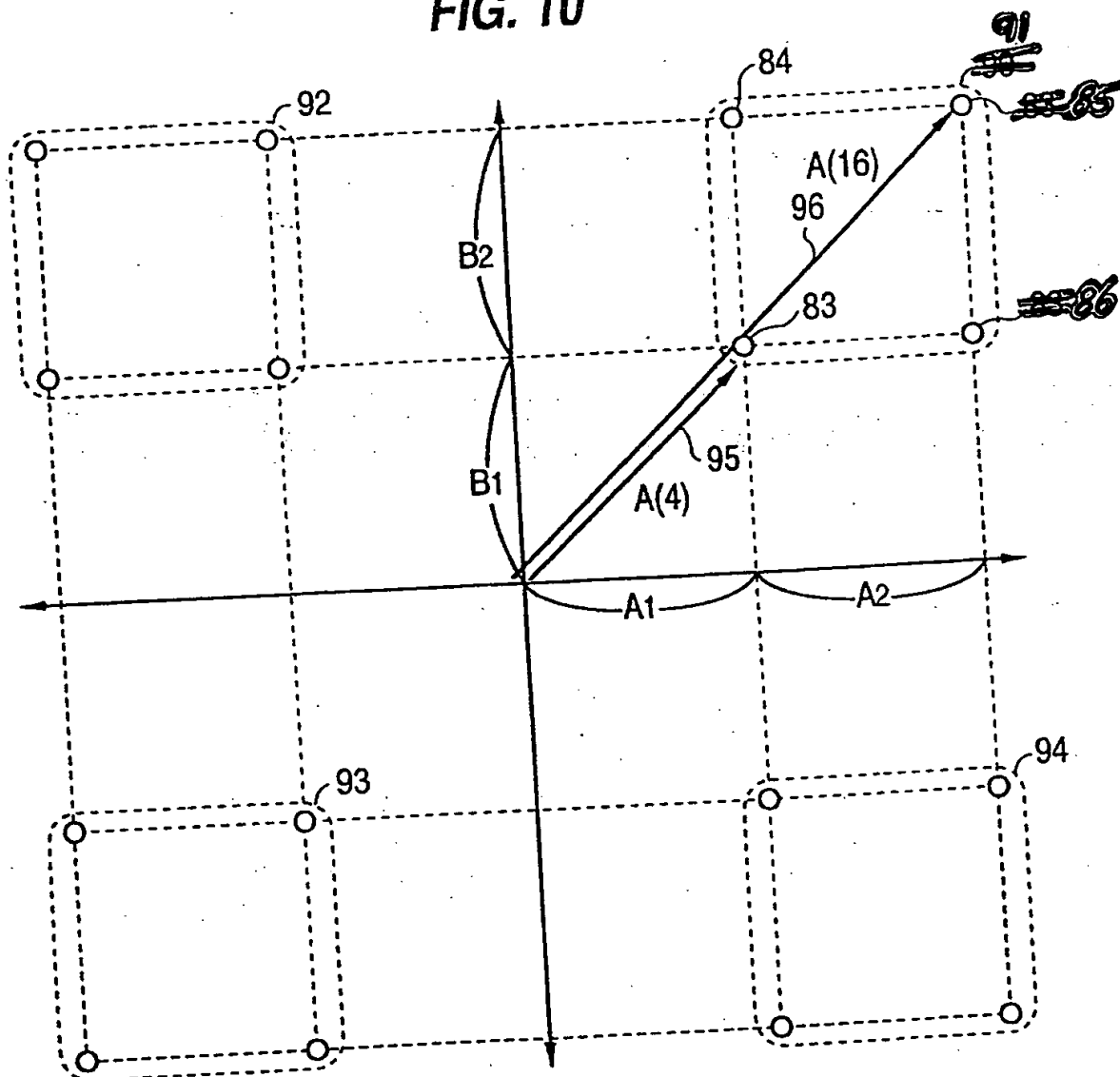
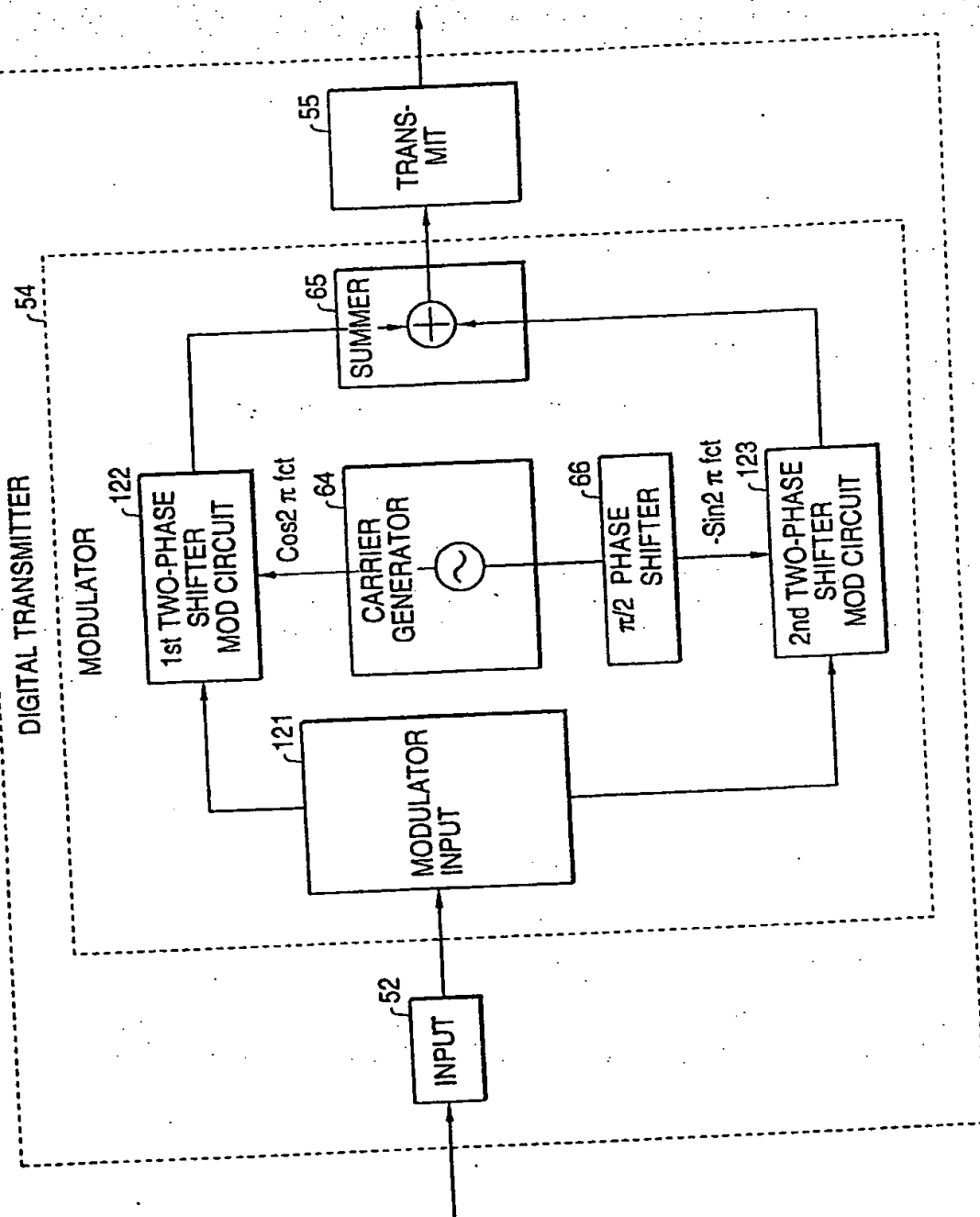


FIG. 17



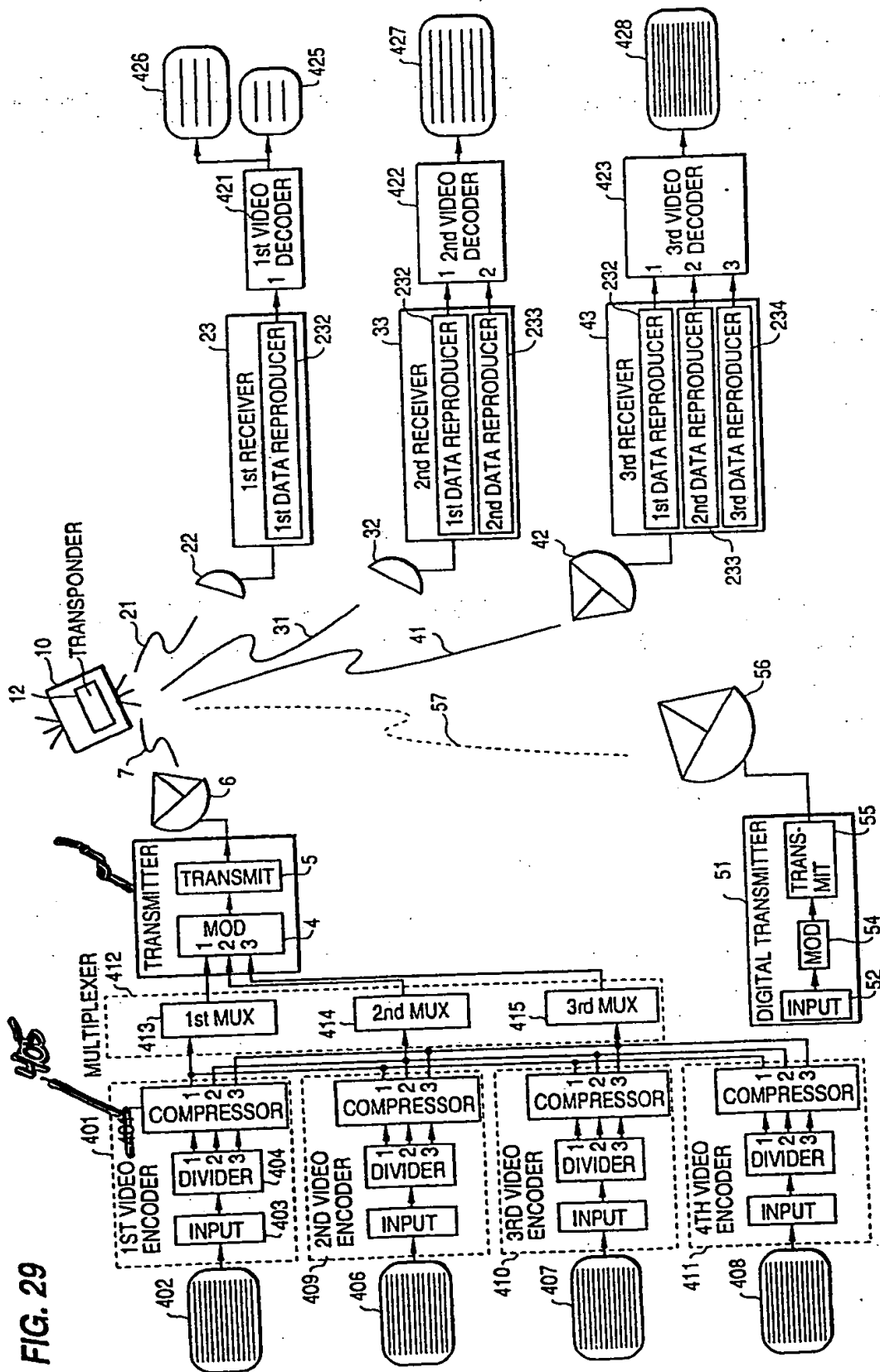


FIG. 48

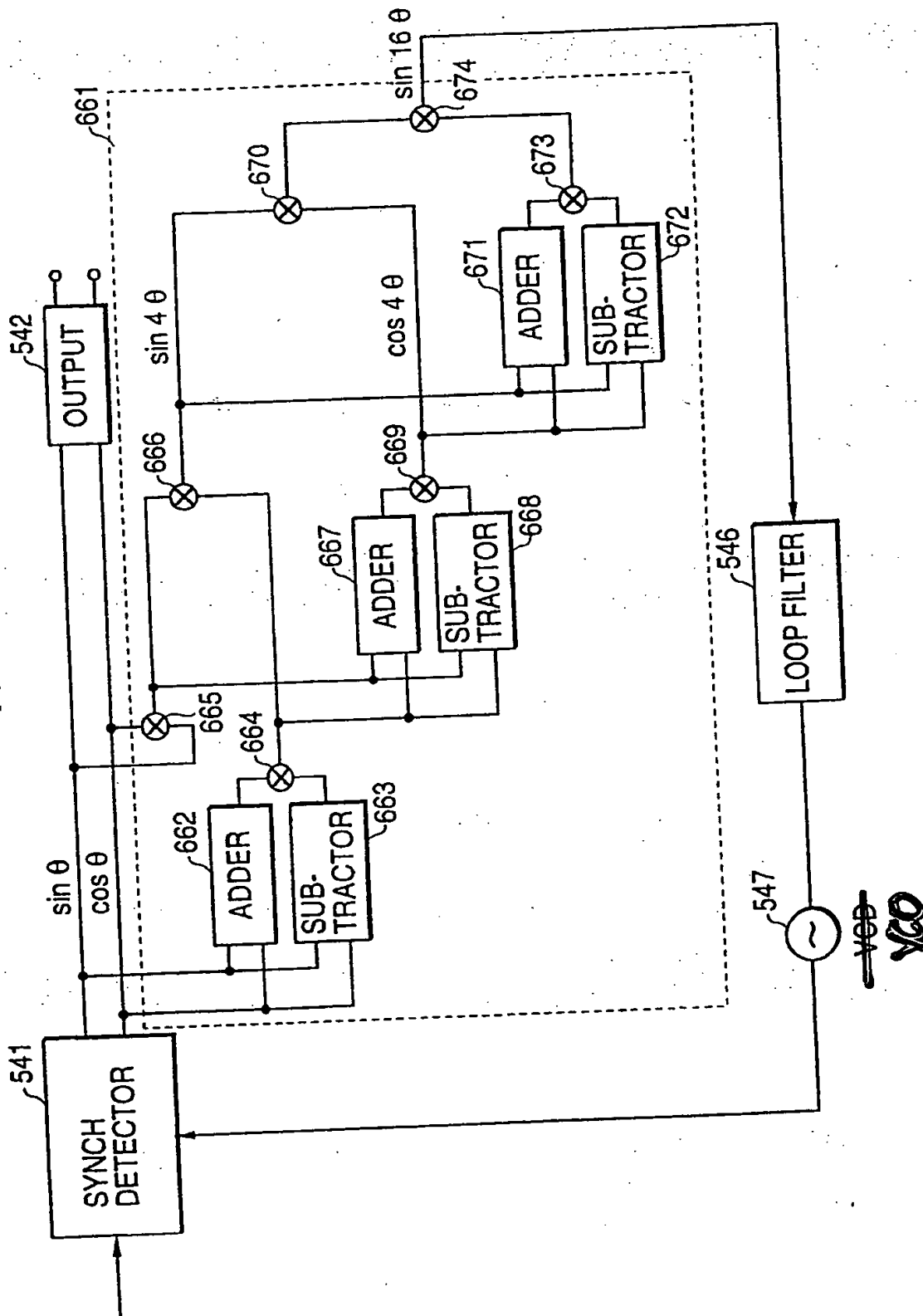


FIG. 65

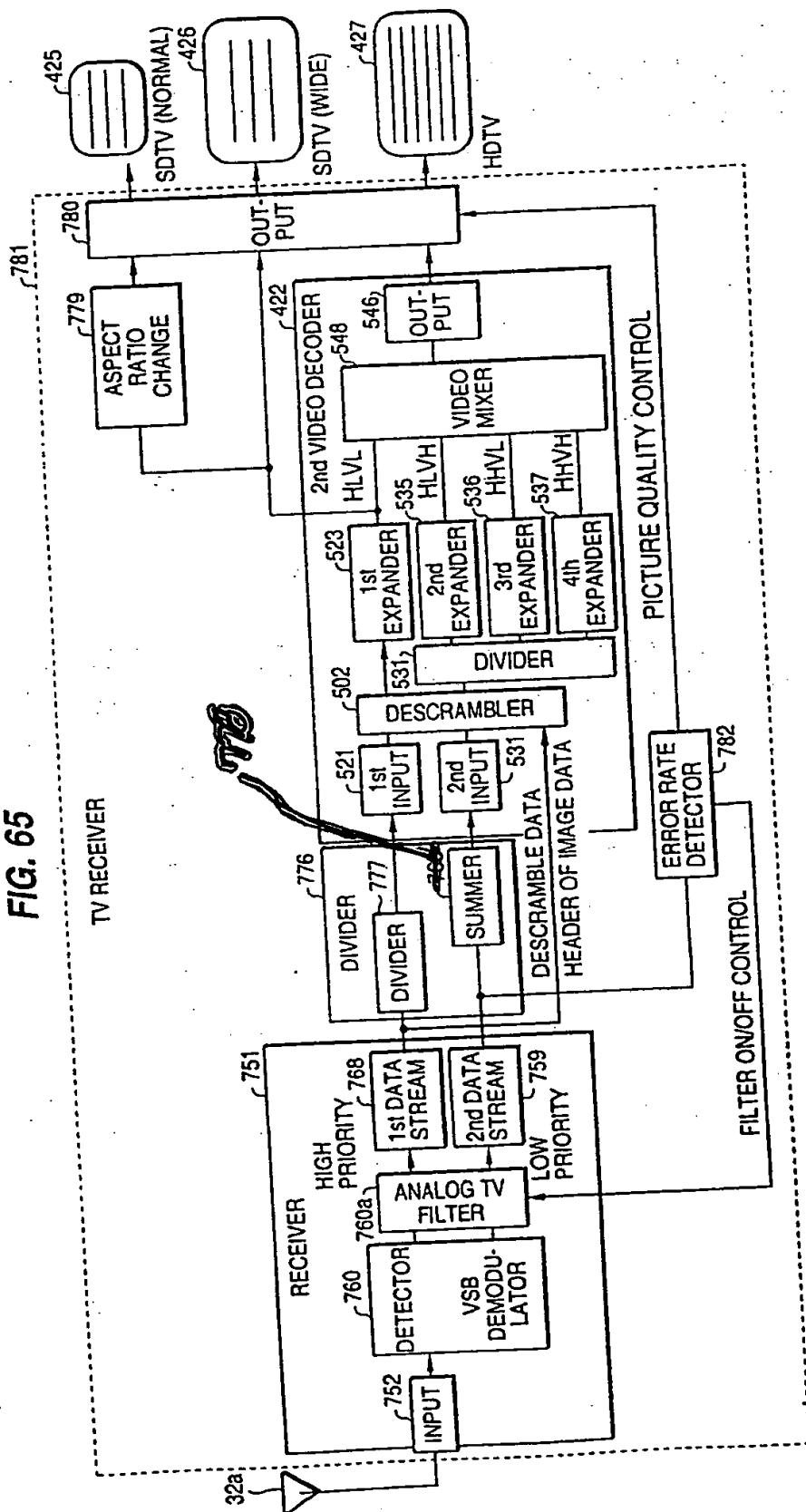


FIG. 67

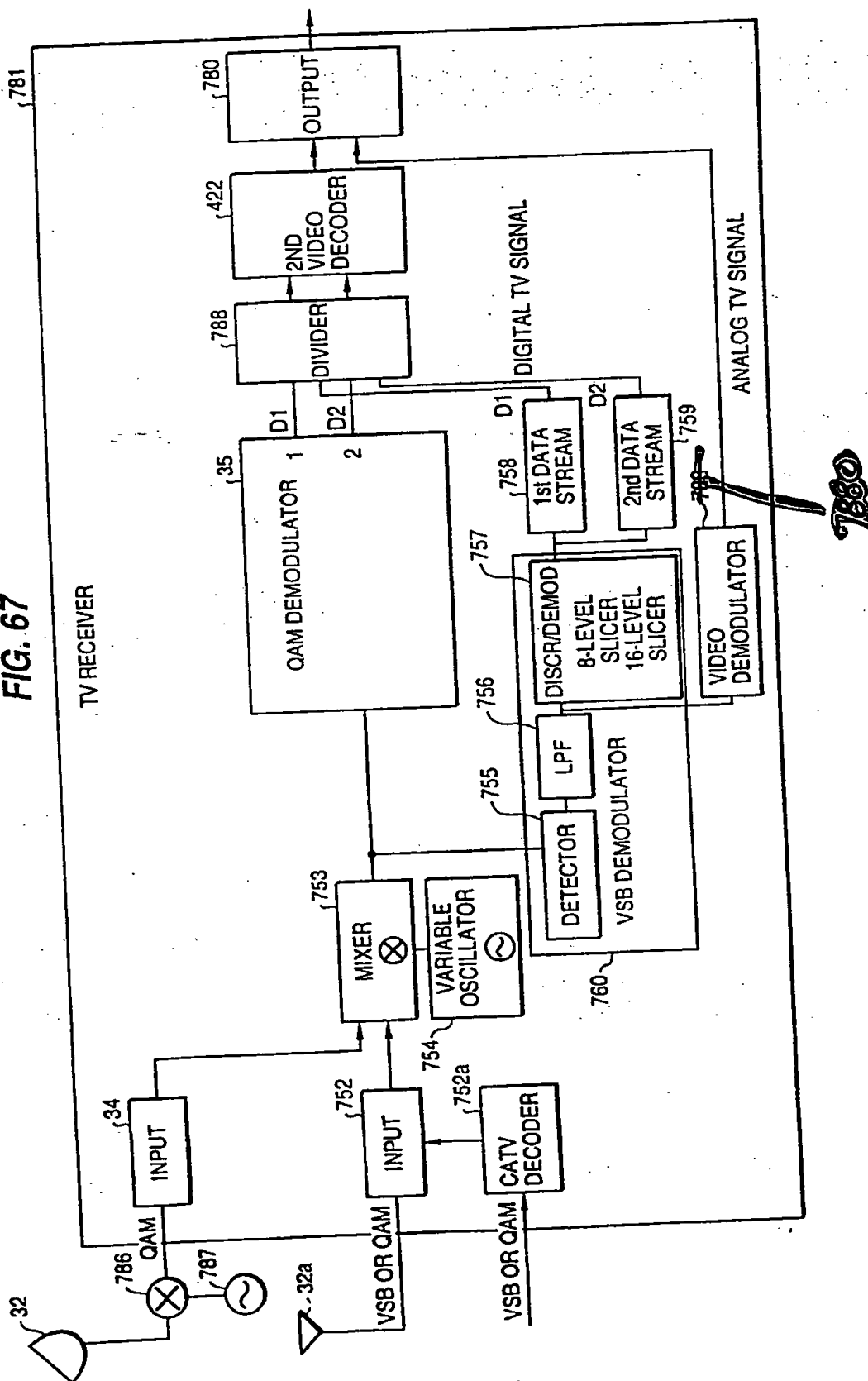


FIG. 93

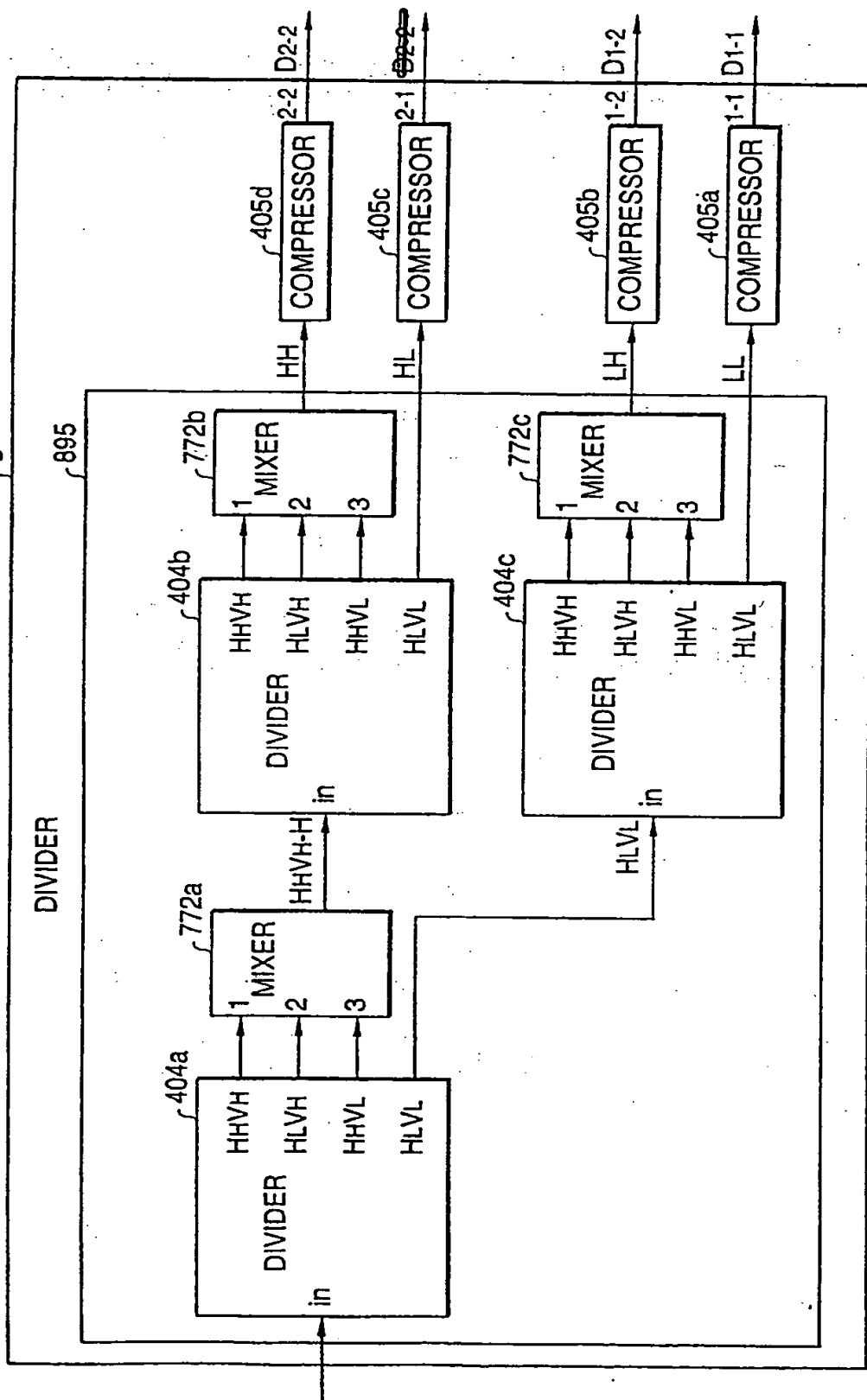
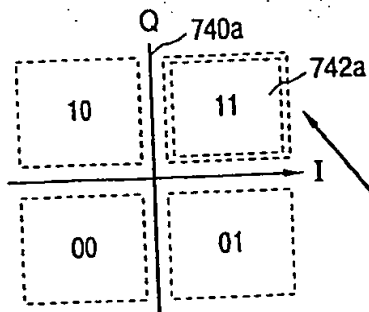


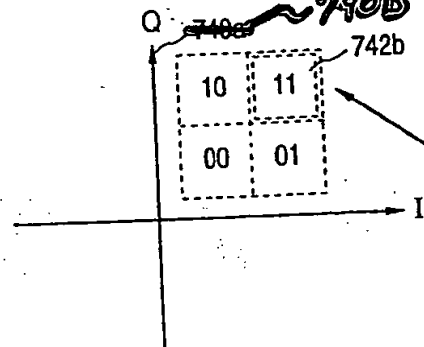


FIG. 112

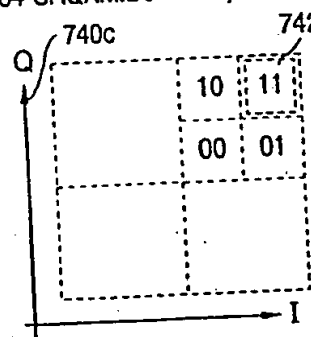
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



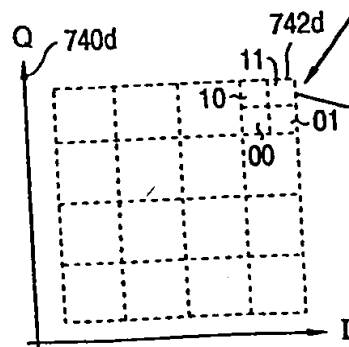
SUBCHANNEL-2 (16-SRQAM:D2 = 2bit)



SUBCHANNEL-3 (64-SRQAM:D3 = 2bit)



SUBCHANNEL-4 (256-SRQAM:D4 = 2bit)



CODE WORD-1

CODE WORD-2

CODE WORD-3

CODE WORD-4

741a 11 2bit  
741b 11 2bit

741c 11 2bit

741d 11 2bit

8bit

SIGNAL POINT  
CODE WORD  
11 11 11 11

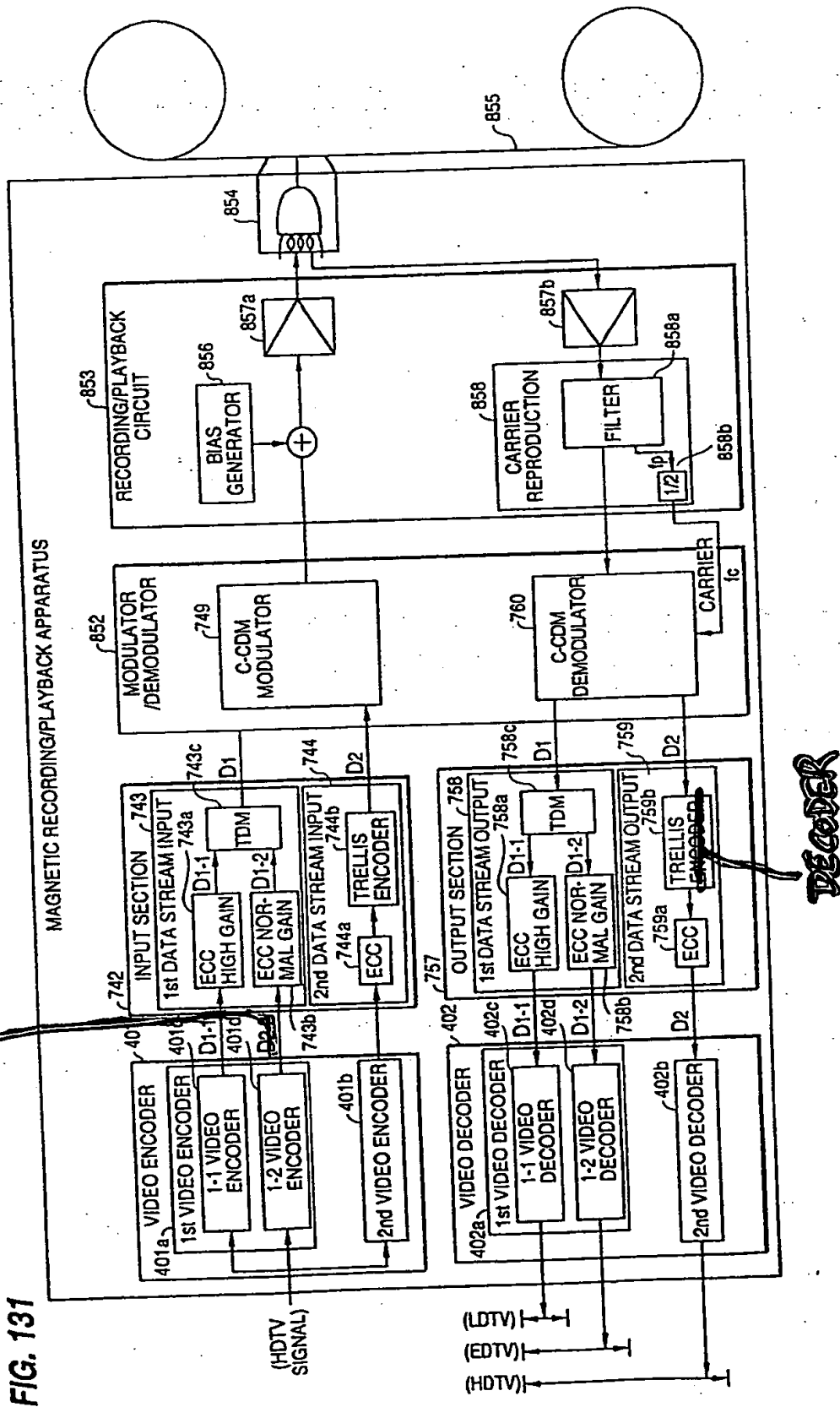


FIG. 138

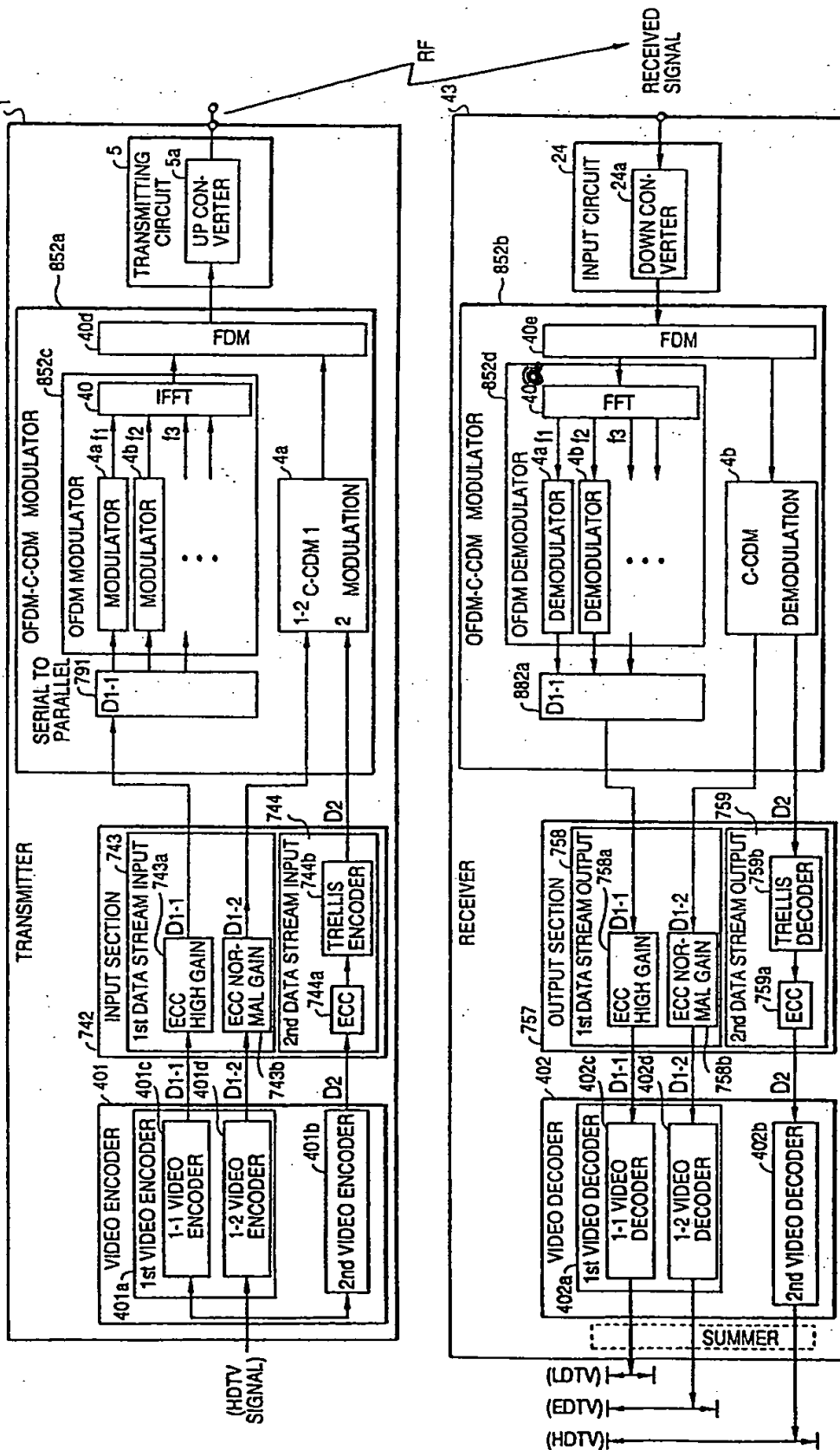


FIG. 144

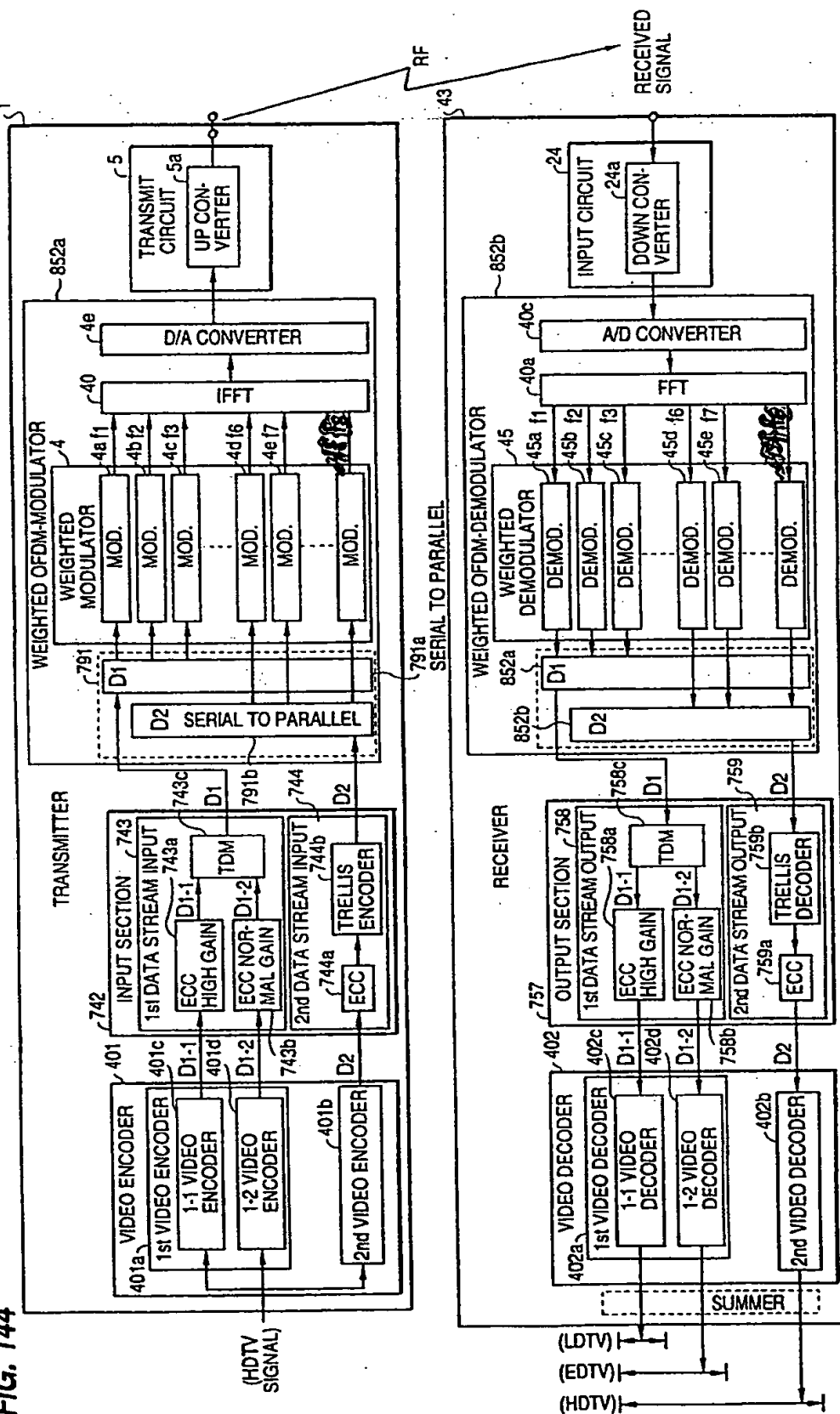


FIG. 169

